

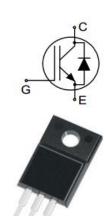
IGBT

Features

- 650V,25A
- $V_{CE(sat)(typ.)}$ =2.0 $V@V_{GE}$ =15V, I_{C} =25A
- High speed switching
- Higher system efficiency
- Soft current turn-off waveforms
- Square RBSOA using NPT technology



JIAEN NPT IGBTs offer lower losses and higher energy efficiency for application such as IH (induction heating),UPS, general inverter and other soft switching applications.



Absolute Maximum Ratings

Symbol	Parameter	Value	Units
Vces	Collector-Emitter Voltage	650	V
V _{GES}	Gate-Emitter Voltage	<u>+</u> 30	V
	Continuous Collector Current (Tc=25 °C)	50	А
lc	Continuous Collector Current (Tc=100°C)	25	А
Ісм	Pulsed Collector Current (Note 1)	75	А
I _F	Diode Continuous Forward Current (T _C =100 °C)	25	А
lғм	Diode Maximum Forward Current (Note 1)	75	А
t _{sc}	Short Circuit Withstand Time	10	us
Б	Maximum Power Dissipation (T _C =25 ℃)	42	W
P_D	Maximum Power Dissipation (T _C =100 °C)	16.7	W
ТJ	Operating Junction Temperature Range	-55 to +150	$^{\circ}$
Tstg	Storage Temperature Range	-55 to +150	$^{\circ}$

Thermal Characteristics

Symbol	Parameter	Max.	Units
R _{th j-c}	Thermal Resistance, Junction to case for IGBT	3.0	°C/ W
R _{th j-c}	Thermal Resistance, Junction to case for Diode	2.9	°C/W
R _{th j-a}	Thermal Resistance, Junction to Ambient	65	°C/W

JNG25T65FS1

$\underline{\textbf{Electrical Characteristics}} \text{ (Tc=25\,°C unless otherwise noted)}$

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
BV _{CES}	Collector-Emitter Breakdown Voltage	$V_{GE} = 0V, I_{C} = 250uA$	650	-	-	V
I _{CES}	Collector-Emitter Leakage Current	V _{CE} = 650V, V _{GE} = 0V	-	-	100	uA
I _{GES}	Gate Leakage Current, Forward	$V_{GE} = + 20V, V_{CE} = 0V$	-	•	<u>+</u> 100	nA
$V_{GE(th)}$	Gate Threshold Voltage	$V_{GE} = V_{CE}$, $I_{C} = 250uA$	4.5	-	6.5	٧
V _{CE(sat)}	Collector-Emitter Saturation Voltage	V _{GE} =15V, I _C = 40A	-	2.0	2.8	V
Qg	Total Gate Charge	Vcc=480V	-	51		nC
Q _{ge}	Gate-Emitter Charge	V _{GE} =15V	-	6.3		nC
Qgc	Gate-Collector Charge	Ic=25A	-	32		nC
t _{d(on)}	Turn-on Delay Time		-	21	-	ns
t r	Turn-on Rise Time	V _{cc} =400V	-	37	-	ns
t d(off)	Turn-off Delay Time	V _{GE} =15V	-	121	-	ns
t f	Turn-off Fall Time	Ic=25A R _G =15Ω Inductive Load T _C =25 $^{\circ}$ C	-	77	-	ns
Eon	Turn-on Switching Loss		-	0.6	-	mJ
Eoff	Turn-off Switching Loss		-	0.6	-	mJ
Ets	Total Switching Loss		-	1.2	-	mJ
C _{ies}	Input Capacitance	V _{CE} =25V V _{GE} =0V	-	856	-	pF
Coes	Output Capacitance		-	96	-	pF
C _{res}	Reverse Transfer Capacitance	f = 1MHz	-	12	-	pF

Electrical Characteristics of Diode (Tc=25°C unless otherwise noted)

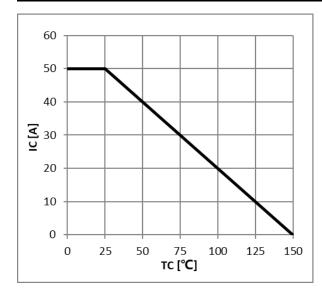
Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
V _F	Diode Forward Voltage	I _F =25A	-	1.5	2.45	V
trr	Diode Reverse Recovery Time	V _{CE} = 400V	-	68		ns
Irr	Diode peak Reverse Recovery Current	I _F = 25A	-	23.8		Α
Qrr	Diode Reverse Recovery Charge	Rg=15 Ω	-	859		nC

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature



Typical Performance Characteristics



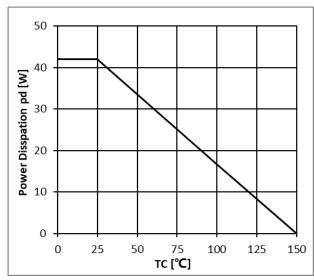
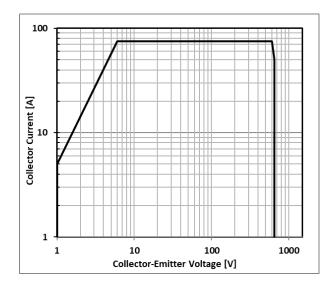


Figure 1: Maximum DC Collector Current VS. case temprature

Figure 2: Power Dissipation VS. Case Temperature



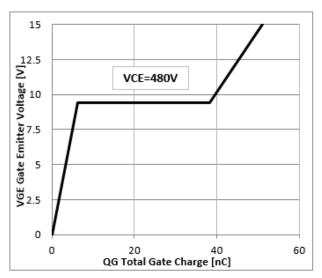
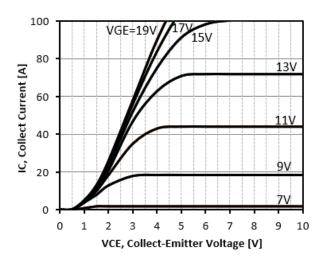


Figure 3: Reverse Bias SOA,TJ=125℃,VGE=15V

Figure 4: Typical Gate charge VS. VGE,IC=25A





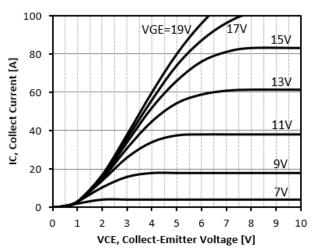
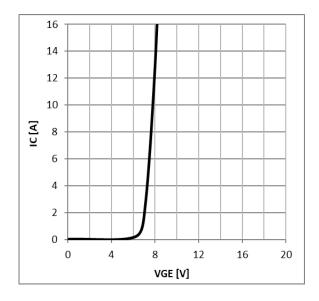


Figure 5: Typical IGBT Output characteristics, $\label{eq:TC=25\,C;tp=300us} TC = 25\,^{\circ}C; tp = 300us$

Figure 6: Typical IGBT Output characteristics, C=150°C;tp=300us



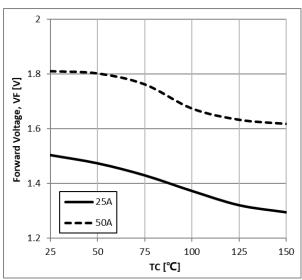


Figure 7: Typical Gate Threshold Voltage

Figure 8: Typical Forward Voltage vs IF



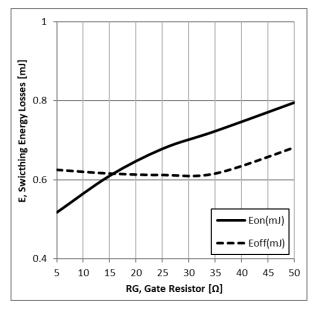


Figure 9: Typical Energy Loss VS. RG, TC= 25° C, L=200uH,VCE=400V,VGE=15V,IC=25A

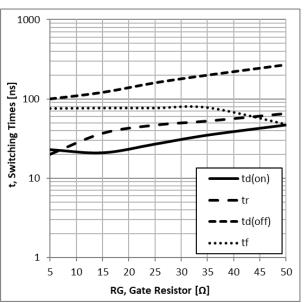


Figure 10: Typical Switching Time VS. RG, TC=25℃, L=200uH,VCE=400V,VGE=15V,IC=25A

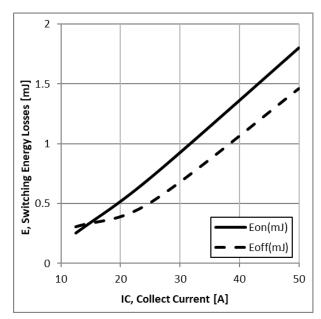


Figure 11: Typical Energy Loss VS. IC,TC=25 $^{\circ}$ C, L=200uH, VCE=400V, VGE=15V,RG=15 $^{\Omega}$

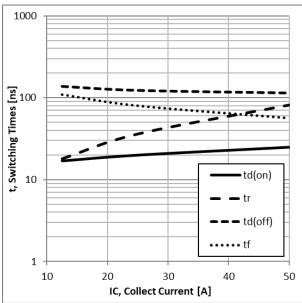


Figure 12: Typical Switching Time VS. IC,TC=25 $^{\circ}$, L=200uH,VCE=400V,VGE=15V,RG=15 $^{\Omega}$





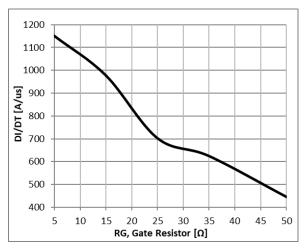


Figure 13: Typical Diode DI/DT VS. RG,TC= 25° C VCC=400V, VGE=15V, IF=25A

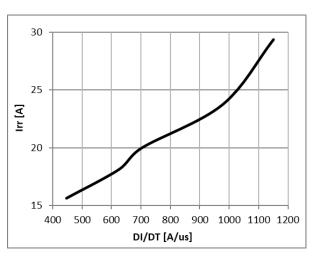


Figure 14: Typical Diode IRR VS. DI/DT,TC=25℃ VCC=400V,VGE=15V, IF=25A

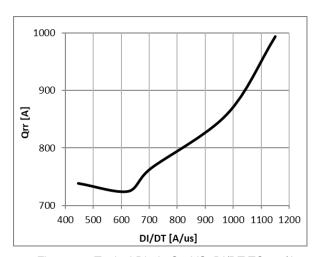


Figure 15: Typical Diode Qrr VS. DI/DT,TC=25℃ VCC=400V, VGE=15V, IF=25A

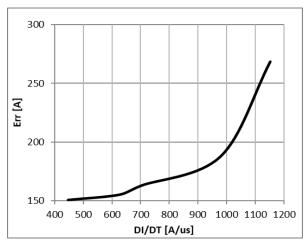


Figure 16: Typical Diode Err VS. DI/DT,TC=25℃ VCC=400V, VGE=15V, IF=25A



JNG25T65FS1

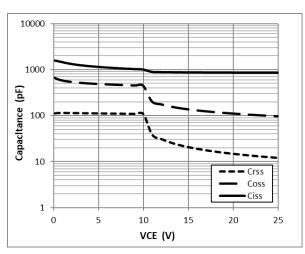


Figure 17: Typical Capacitance VS. VCE, $VGE {=} 0V, f {=} 1MHz \label{eq:VGE}$

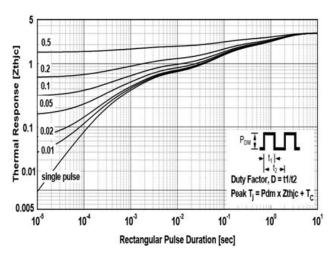
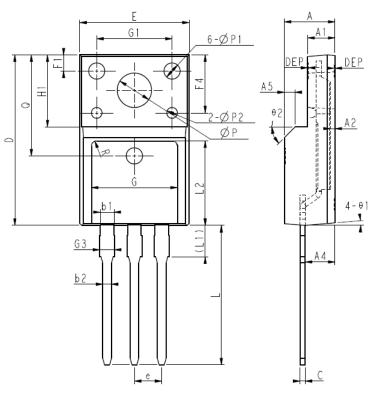


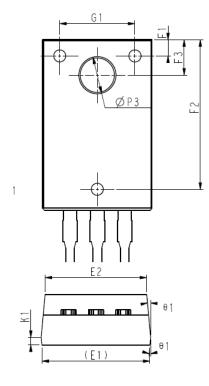
Figure 18: Normalized transient thermal impedance junction-to-case



TO-220F PACKAGE OUTLINE



COMMON DIMENSIONS



SYMBOL	MM			
SIMDUL	MIN	NOM	MAX	
Е	10.00	10. 16	10.32	
E1	9. 94	10.04	10.14	
E2	9. 36	9.46	9. 56	
Α	4. 50	4.70	4. 90	
A1	2. 34	2.54	2.74	
A2	0.43	-	0.48	
A4	2.66	2.76	2.86	
A5		1. 00REF		
С	0.45	0.50	0.60	
D	15.67	15. 87	16.07	
Q		9. 40REF		
H1		6. 70REF		
е		2. 54BSC		
ΦР		3. 18REF		
L	12. 78	12.98	13. 18	
L1	2.83	2.93	3.03	
L2	7.70	7.80	7.90	
ФР1	1.40	1.50	1.60	
ФР2	0.95	1.00	1.05	
ФР3		3. 45REF		
θ 1	3°	5°	7°	
θ 2	-	45°	-	
DEP	0.05	0.10	0.15	
F1	1.00	1.50	2.00	
F2	13.80	13.90	14.00	
F3	3. 20	3. 30	3. 40	
F4	5. 30	5. 40	5. 50	
G	7. 80	8.00	8. 20	
G1	6. 90	7.00	7. 10	
G3	1. 25	1.35	1. 45	
b1	1. 23	1.28	1. 38	
b2	0.75	0.80	0.90	
K1	0.65	0.70	0.75	
R		0. 50REF		



JNG25T65FS1

Disclaimers

JIAEN Semiconductor Co., Ltd reserves the right to make changes without notice in order to improve reliability, function or design and to discontinue any product or service without notice. Customers should obtain the latest relevant information before orders and should verify that such information is current and complete. All products are sold subject to JIAEN's terms and conditions supplied at the time of order acknowledgement.

JIAEN Semiconductor Co., Ltd warrants performance of its hardware products to the specifications at the time of sale, Testing, reliability and quality control are used to the extent JIAEN deems necessary to support this warrantee. Except where agreed upon by contractual agreement, testing of all parameters of each product is not necessarily performed.

JIAEN Semiconductor Co., Ltd does not assume any liability arising from the use of any product or circuit designs described herein. Customers are responsible for their products and applications using JIAEN's components. To minimize risk, customers must provide adequate design and operating safeguards.

JIAEN Semiconductor Co., Ltd does not warrant or convey any license either expressed or implied under its parent rights, nor the rights of others. Reproduction of information in JIAEN's datasheets or data books sis permissible only if reproduction is without modification or alteration. Reproduction of this information with any alteration is an unfair and deceptive business practice. JIAEN Semiconductor Co., Ltd is not responsible or liable for such altered documentation.

Resale of JIAEN's products with statements different from or beyond the parameters stated by JIAEN Semiconductor Co., Ltd for that product or service voids all express or implied warrantees for the associated JIAEN's product or service and is unfair and deceptive business practice. JIAEN Semiconductor Co., Ltd is not responsible or liable for any such statements.