

IGBT

Features

- 1200V,15A
- V_{CE(sat)(typ.)}=2.2V@V_{GE}=15V,I_C=15A
- High speed switching
- Higher system efficiency
- Soft current turn-off waveforms
- Square RBSOA using NPT technology

General Description

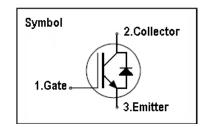
JIAEN NPT IGBTs offer lower losses and higher energy efficiency for application such as IH (induction heating),UPS, general inverter and other soft switching applications.

Absolute Maximum Ratings

Symbol	Parameter	Value	Units
V _{CES}	Collector-Emitter Voltage	1200	V
V _{GES}	Gate-Emitter Voltage	<u>+</u> 20	V
	Continuous Collector Current ($T_c=25$ °C)	30	А
lc	Continuous Collector Current (T _c =100°C)	15	A
I _{СМ}	Pulsed Collector Current (Note 1)	45	А
I _F	Diode Continuous Forward Current ($T_C=100$ °C)	15	A
I _{FM}	Diode Maximum Forward Current (Note 1)	45	А
t _{sc}	Short Circuit Withstand Time	10	us
P _D	Maximum Power Dissipation ($T_c=25$ °C)	175	W
	Maximum Power Dissipation (T _c =100°C)	70	W
TJ	Operating Junction Temperature Range	-55 to +150	°C
T _{STG}	Storage Temperature Range	-55 to +150	°C

Thermal Characteristics

Symbol	Parameter	Max.	Units
R _{th j-c}	Thermal Resistance, Junction to case for IGBT	0.70	°C/ W
R _{th j-c}	Thermal Resistance, Junction to case for Diode	0.98	°C/W
R _{th j-a}	R _{th j-a} Thermal Resistance, Junction to Ambient		°C/W



JNG15N120AI



Electrical Characteristics ($T_c=25$ °C unless otherwise noted)

Symbol	Parameter			Тур.	Max.	Units
BV _{CES}	Collector-Emitter Breakdown Voltage	$V_{GE} = 0V, I_{C} = 250uA$	1200	-	-	V
I _{CES}	Collector-Emitter Leakage Current	V _{CE} = 1200V, V _{GE} = 0V	-	-	250	uA
	Gate Leakage Current, Forward	V_{GE} =30V, V_{CE} = 0V	-	-	100	nA
GES	Gate Leakage Current, Reverse	V_{GE} = -30V, V_{CE} = 0V	-	-	-100	nA
$V_{GE(th)}$	Gate Threshold Voltage	$V_{GE} = V_{CE}, I_C = 250 \text{uA}$	4	-	6	V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	V _{GE} =15V, I _C = 15A	-	2.2	2.7	V
Qg	Total Gate Charge	V _{cc} =960V	-	70		nC
Qge	Gate-Emitter Charge	V _{GE} =15V	-	23		nC
Q _{gc}	Gate-Collector Charge	I _C =15A	-	24		nC
t _{d(on)}	Turn-on Delay Time		-	33	-	ns
t r	Turn-on Rise Time	V _{cc} =600V	-	37	-	ns
t _{d(off)}	Turn-off Delay Time	V _{GE} =15V	-	260	-	ns
t _f	Turn-off Fall Time	− I _C =15A _ R _G =28Ω	-	135	-	ns
Eon	Turn-on Switching Loss	Inductive Load	-	1.3	-	mJ
Eoff	Turn-off Switching Loss	T _C =25 ℃	-	0.9	-	mJ
Ets	Total Switching Loss]	-	2.2	-	mJ
Cies	Input Capacitance	V _{CE} =25V	-	550	-	pF
Coes	Output Capacitance	V _{GE} =0V	-	180	-	pF
C _{res}	Reverse Transfer Capacitance	f = 100kHz	-	110	-	pF

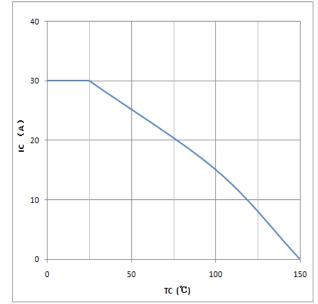
Electrical Characteristics of Diode (Tc=25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
V _F	Diode Forward Voltage	I _F =15A	-	2.0	2.8	V
trr	Diode Reverse Recovery Time	V _{CE} = 600V	-	255		ns
l _{rr}	Diode peak Reverse Recovery Current	I _F = 15A	-	13		А
Q _{r r}	Diode Reverse Recovery Charge	$dI_F/dt = 400 A/us$	-	2015		nC

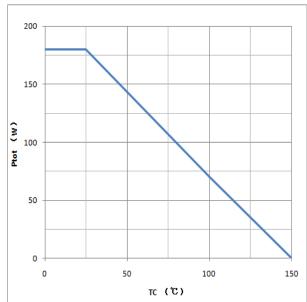
Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature









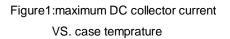


Figure2:power dissipation VS. case temprature

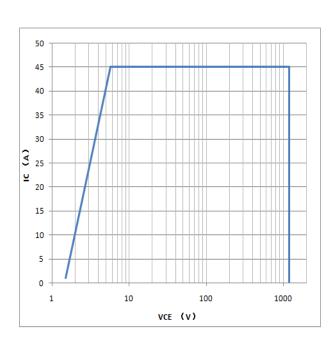
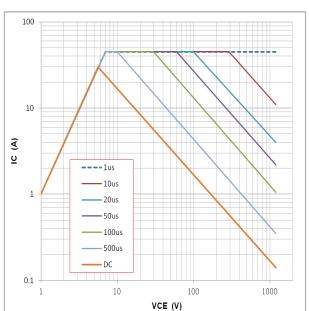
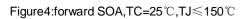
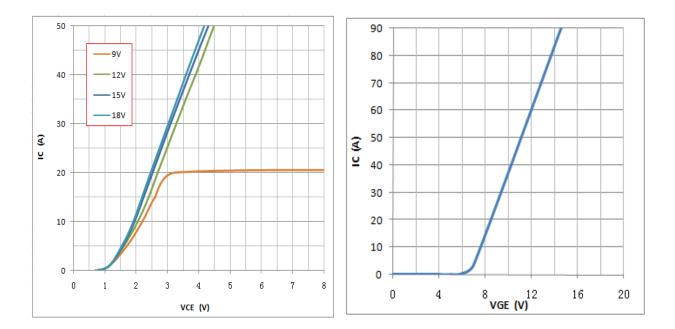


Figure3:reverse bias SOA,TJ=150°C,VGE=15V









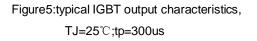
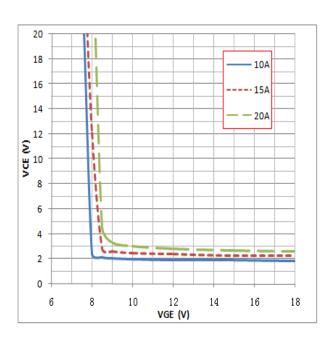


Figure6:typical trans characteristics,VCE=20V,tp=20us



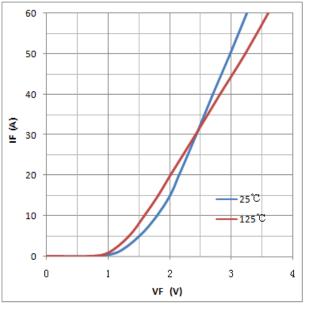


Figure7: typical VCE VS. VGE,TJ=25°C

Figure8:typical diode forward characteristic,tp=300us



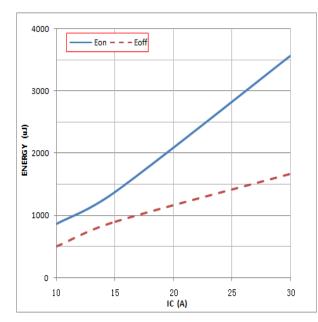


Figure9: typical energy loss VS. IC, TC=25°C,

L=500uH, VCE=600V,VGE=15V,Rg=28 Ω

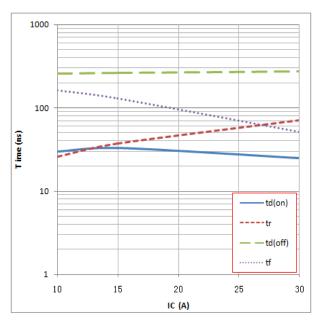


Figure10: typical switching time VS. IC, TC=25°C,

L=500uH, VCE=600V,VGE=15V,Rg=28Ω

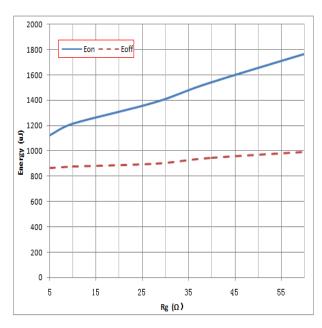


Figure11: typical energy loss VS. Rg,TC=25°C,

L=500uH, VCE=600V, VGE=15V,IC=15A

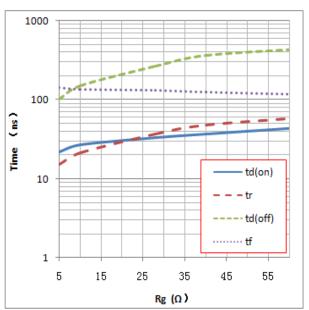


Figure 12: typical switching time VS. Rg,TC=25°C,

L=500uH,VCE=600V,VGE=15V,IC=15A



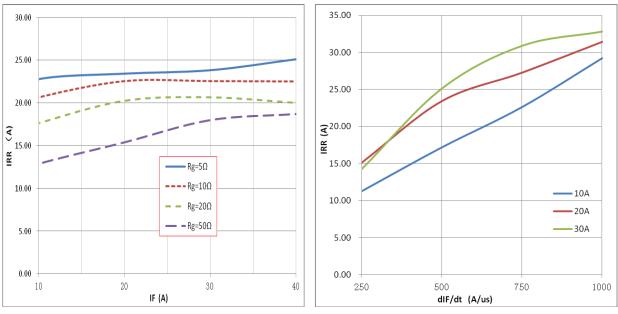
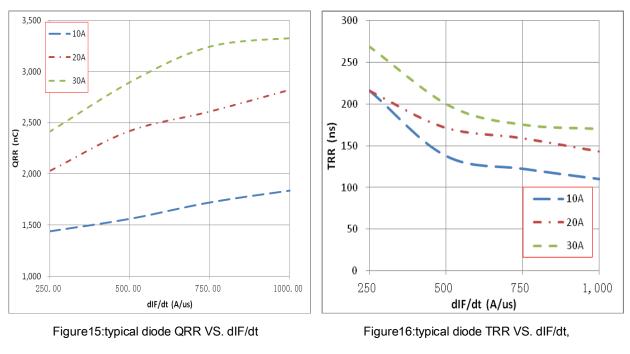


Figure13: typical diode IRR VS. IF, TC=25°C

VCC=600V, VGE=15V

Figure14:typical diode IRR VS. dIF/dt

VCC=600V,VGE=15V

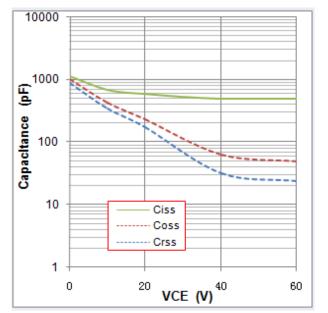


VCC=600V,VGE=15V

VCC=600V,VGE=15V

www.jiaensemi.com





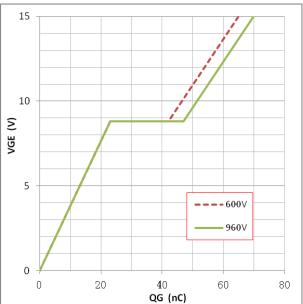


Figure17:typical capacitance VS. VCE, VGE=0V, f=100kHz Figure18:typical gate charge VS. VGE, IC=15A

TO-3P PACKAGE OUTLINE

@ Max 4 2mm В ტ ۵ υ ĸ D TO-3P Ins

	Dimensions						
Ref.	Millimeters			Inches			
	Min.	Тур.	Max.	Min.	Тур.	Max.	
A	4.40		4.60	0.173		0.181	
В	1.45		1.55	0.057		0.061	
С	14.35		15.60	0.565		0.614	
D	0.50		0.70	0.020		0.028	
E	2.70		2.90	0.106		0.114	
F	15.80		16.50	0.622		0.650	
G	20.40		21.10	0.803		0.831	
н	15.10		15.50	0.594		0.610	
J	5.40		5.65	0.213		0.222	
к	1.10		1.40	0.043		0.055	
L	1.35		1.50	0.053		0.059	
Р	2.80		3.00	0.110		0.118	
R		4.35			0.171		



Disclaimers

JIAEN Semiconductor Co., Ltd reserves the right to make changes without notice in order to improve reliability, function or design and to discontinue any product or service without notice. Customers should obtain the latest relevant information before orders and should verify that such information is current and complete. All products are sold subject to JIAEN's terms and conditions supplied at the time of order acknowledgement.

JIAEN Semiconductor Co., Ltd warrants performance of its hardware products to the specifications at the time of sale, Testing, reliability and quality control are used to the extent JIAEN deems necessary to support this warrantee. Except where agreed upon by contractual agreement, testing of all parameters of each product is not necessarily performed.

JIAEN Semiconductor Co., Ltd does not assume any liability arising from the use of any product or circuit designs described herein. Customers are responsible for their products and applications using JIAEN's components. To minimize risk, customers must provide adequate design and operating safeguards.

JIAEN Semiconductor Co., Ltd does not warrant or convey any license either expressed or implied under its parent rights, nor the rights of others. Reproduction of information in JIAEN's datasheets or data books sis permissible only if reproduction is without modification or alteration. Reproduction of this information with any alteration is an unfair and deceptive business practice. JIAEN Semiconductor Co., Ltd is not responsible or liable for such altered documentation.

Resale of JIAEN's products with statements different from or beyond the parameters stated by JIAEN Semiconductor Co., Ltd for that product or service voids all express or implied warrantees for the associated JIAEN's product or service and is unfair and deceptive business practice. JIAEN Semiconductor Co., Ltd is not responsible or liable for any such statements.