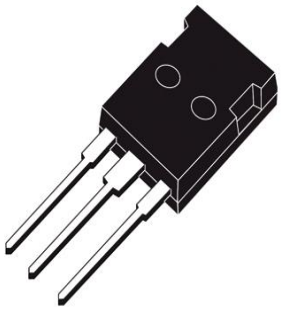
IGBT

Features

- 1200V,50A
- $V_{CE(sat)(typ.)}$=2.1V@V_{GE}=15V,I_C=50A
- High speed switching
- Higher system efficiency
- Soft current turn-off waveforms
- Square RBSOA using NPT technology



TO-247-3L Plus

General Description

JIAEN NPT IGBTs offer lower losses and higher energy efficiency for application such as IH (induction heating),UPS, general inverter and other soft switching applications.

Absolute Maximum Ratings

Symbol	Parameter	Value	Units
V_{CES}	Collector-Emitter Voltage	1200	V
V_{GES}	Gate-Emitter Voltage	± 30	V
I_C	Continuous Collector Current (T_C=25℃)	80	A
	Continuous Collector Current (T_C=100℃)	50	A
I_{CM}	Pulsed Collector Current (Note 1)	150	A
I_F	Diode Continuous Forward Current (T_C=100℃)	50	A
I_{FM}	Diode Maximum Forward Current (Note 1)	150	A
t_{sc}	Short Circuit Withstand Time VGE =15V， Vcc≤960V ，Tj≤150℃；	10	us
P_D	Maximum Power Dissipation (T_C=25℃)	320	W
	Maximum Power Dissipation (T_C=100℃)	130	W
T_J	Operating Junction Temperature Range	-55 to +150	℃
T_{STG}	Storage Temperature Range	-55 to +150	℃

Thermal Characteristics

Symbol	Parameter	Max.	Units
$R_{th\ j\text{-}c}$	Thermal Resistance, Junction to case for IGBT	0.38	℃/ W
$R_{th\ j\text{-}c}$	Thermal Resistance, Junction to case for Diode	0.5	℃/ W
$R_{th\ j\text{-}a}$	Thermal Resistance, Junction to Ambient	40	℃/ W

Electrical Characteristics (T_C=25℃ unless otherwise noted)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
BV_{CES}	Collector-Emitter Breakdown Voltage	V_{GE}= 0V, I_C= 250uA	1200	-	-	V
I_{CES}	Collector-Emitter Leakage Current	V_{CE}= 1200V, V_{GE}= 0V	-	-	250	uA
I_{GES}	Gate Leakage Current, Forward	V_{GE}=30V, V_{CE}= 0V	-	-	100	nA
	Gate Leakage Current, Reverse	V_{GE}= -30V, V_{CE}= 0V	-	-	-100	nA
$V_{GE(th)}$	Gate Threshold Voltage	V_{GE}= V_{CE}, I_C= 250uA	4.0	-	6.0	V
$V_{CE(sat)}$	Collector-Emitter Saturation Voltage	V_{GE}=15V, I_C= 50A	-			
		Tc=25℃		2.1	2.6	V
		Tc=125℃		2.5		V
		Tc=150℃		2.8		V
Q_g	Total Gate Charge	V_{CC}=960V V_{GE}=15V I_C=50A	-	170		nC
Q_{ge}	Gate-Emitter Charge		-	40		nC
Q_{gc}	Gate-Collector Charge		-	80		nC
$t_{d(on)}$	Turn-on Delay Time	V_{CC}=600V V_{GE}=15V I_C=50A R_G=10Ω Inductive Load T_C=25℃	-	50	-	ns
t_r	Turn-on Rise Time		-	80	-	ns
$t_{d(off)}$	Turn-off Delay Time		-	280	-	ns
t_f	Turn-off Fall Time		-	30	-	ns
Eon	Turn-on Switching Loss		-	4.6	-	mJ
Eoff	Turn-off Switching Loss		-	2.8	-	mJ
Ets	Total Switching Loss		-	7.4	-	mJ
C_{ies}	Input Capacitance	V_{CE}=30V V_{GE}=0V f = 1MHz	-	4120	-	pF
C_{oes}	Output Capacitance		-	160	-	pF
C_{res}	Reverse Transfer Capacitance		-	100	-	pF

Electrical Characteristics of Diode (T_C=25℃ unless otherwise noted)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
V_F	Diode Forward Voltage	I_F=50A	-	2.3	3.2	V
t_{rr}	Diode Reverse Recovery Time	V_{CE} = 600V I_F= 50A dI_F/dt = 200A/us	-	250		ns
I_{rr}	Diode peak Reverse Recovery Current		-	10		A
Q_{rr}	Diode Reverse Recovery Charge		-	1350		nC

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature

Typical Performance Characteristics

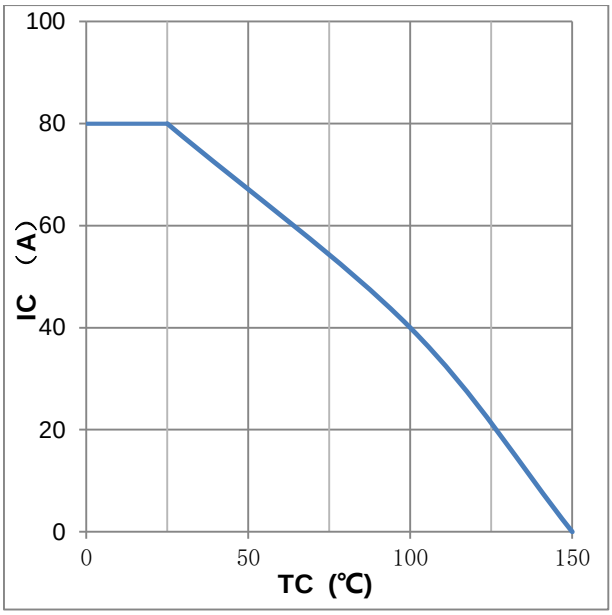


Figure1:maximum DC collector current VS. case temprature

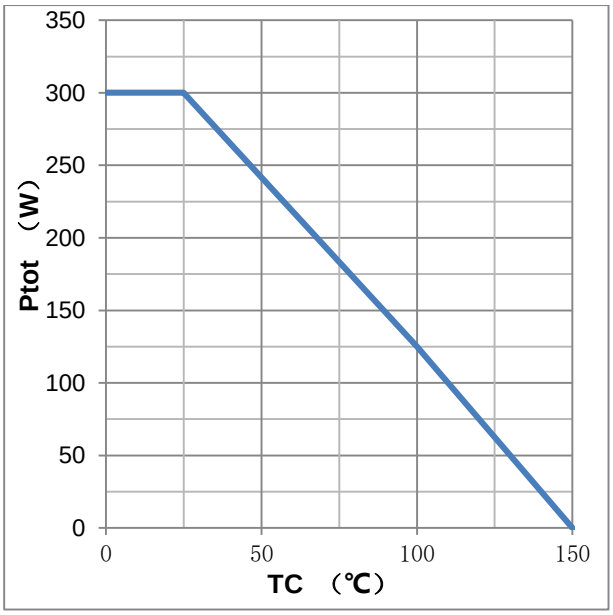


Figure2:power dissipation VS. case temprature

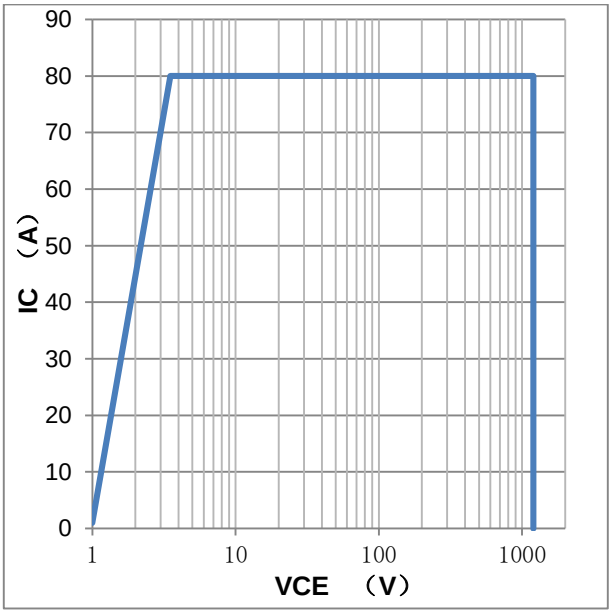


Figure3:reverse bias SOA,TJ=150℃,VGE=15V

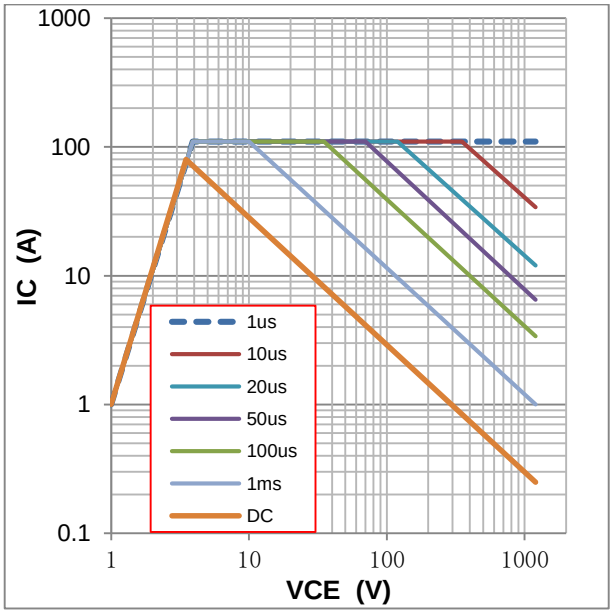


Figure4:forward SOA,TC=25℃,TJ≤150℃

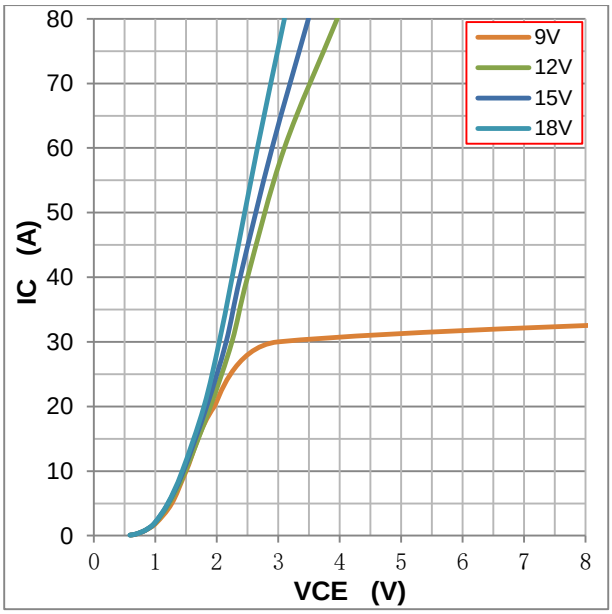


Figure5:typical IGBT output characteristics, TJ=25℃;tp=300us

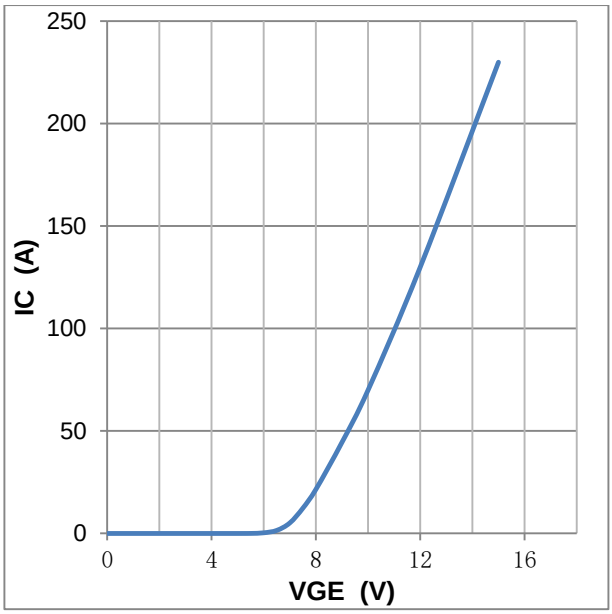


Figure6:typical trans characteristics,VCE=20V,tp=20us

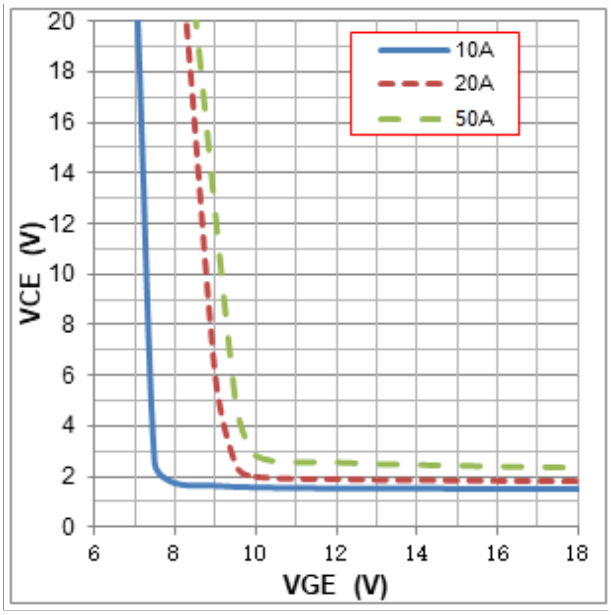


Figure7: typical VCE VS. VGE,TJ=25℃

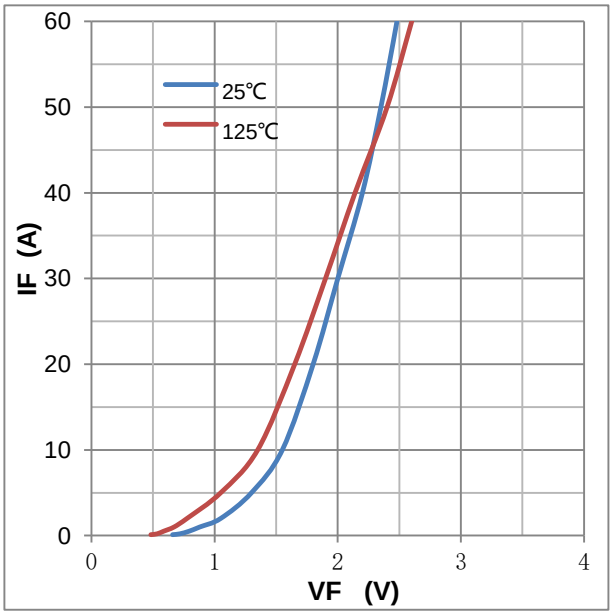


Figure8:typical diode forward characteristic,tp=300us

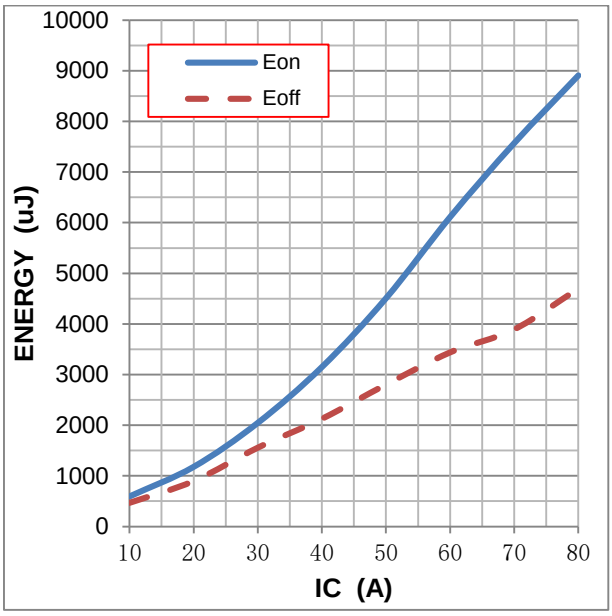


Figure9: typical energy loss VS. IC, TC=25℃,

L=500uH, VCE=600V,VGE=15V,Rg=28Ω

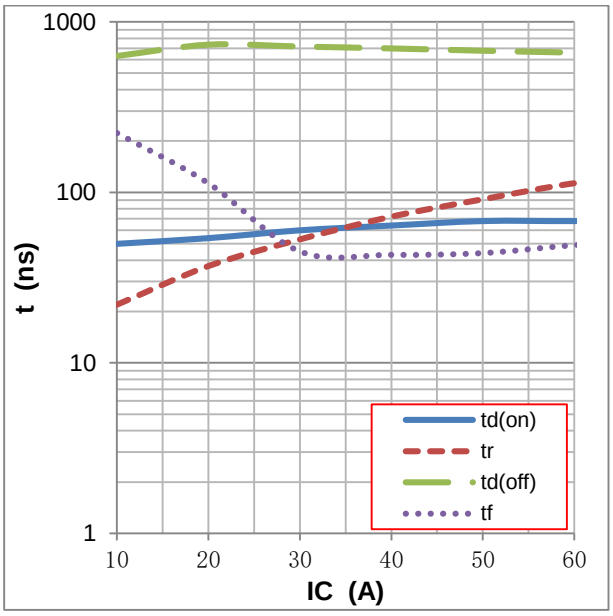


Figure10: typical switching time VS. IC, TC=25℃,

L=500uH, VCE=600V,VGE=15V,Rg=28Ω

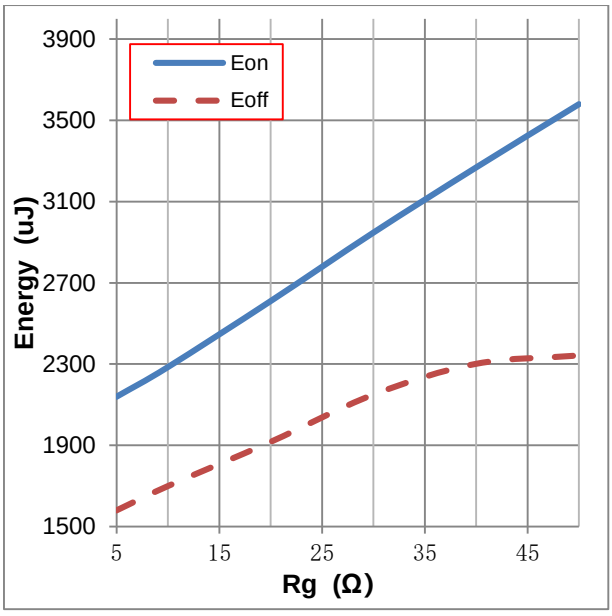


Figure11: typical energy loss VS. Rg,TC=25℃,

L=500uH, VCE=600V, VGE=15V,IC=50A

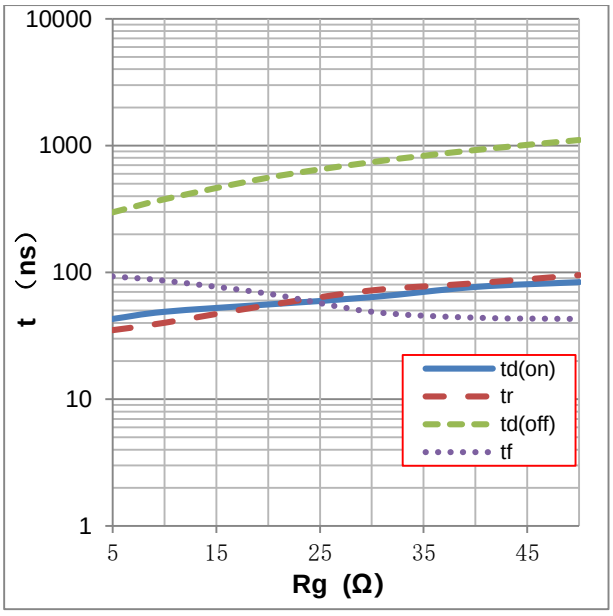


Figure12: typical switching time VS. Rg,TC=25℃,

L=500uH,VCE=600V,VGE=15V,IC=50A

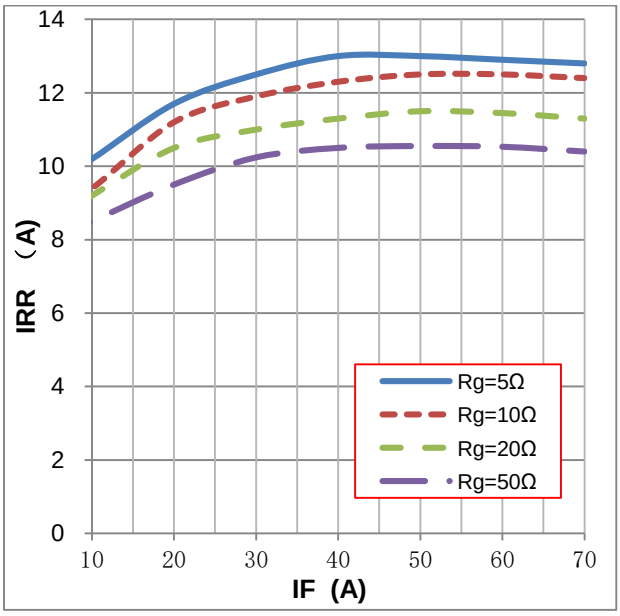


Figure13: typical diode IRR VS. IF, TC=25℃

VCC=600V, VGE=15V

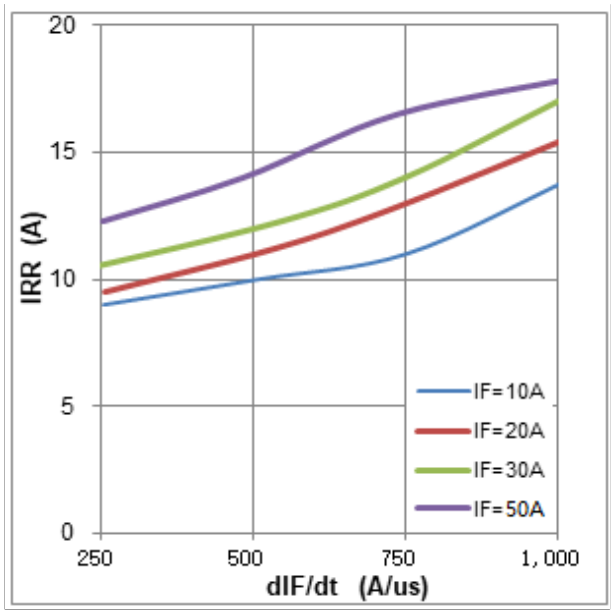


Figure14:typical diode IRR VS. dIF/dt

VCC=600V,VGE=15V

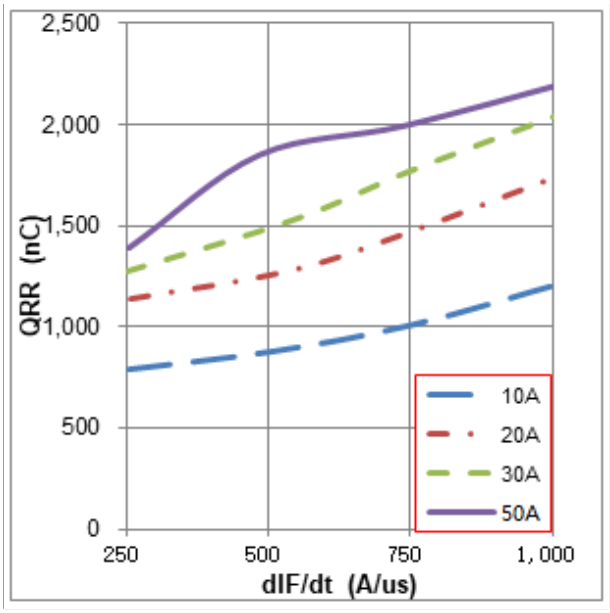


Figure15:typical diode QRR VS. dIF/dt

VCC=600V,VGE=15V

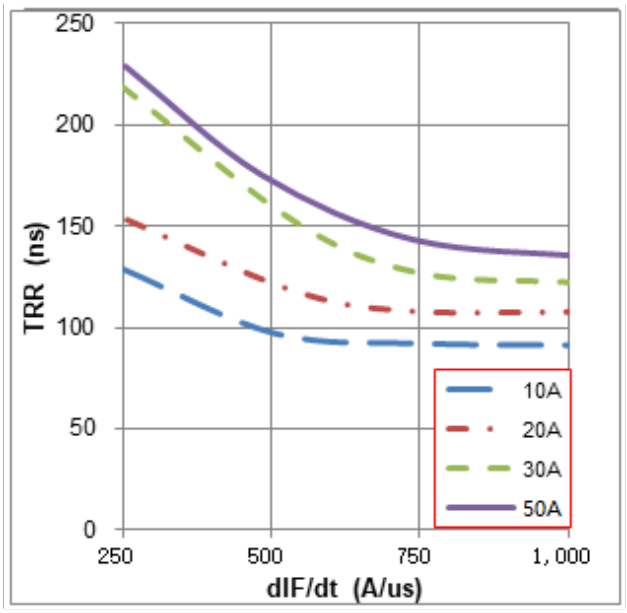


Figure16:typical diode TRR VS. dIF/dt,

VCC=600V,VGE=15V

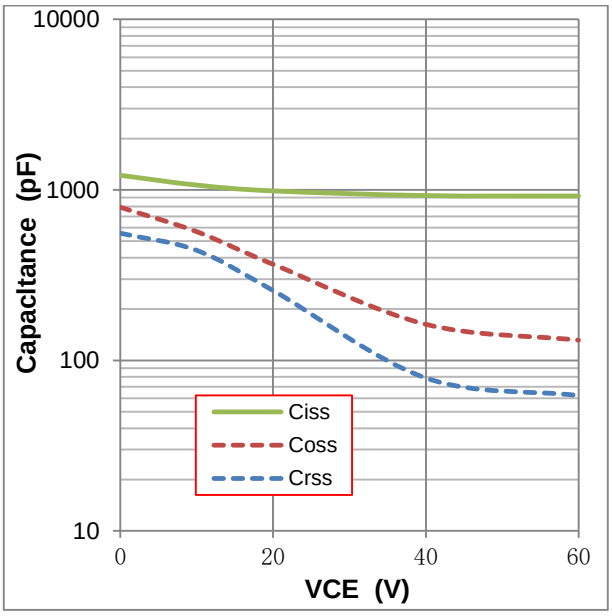


Figure17:typical capacitance VS. VCE,VGE=0V,f=100kHz

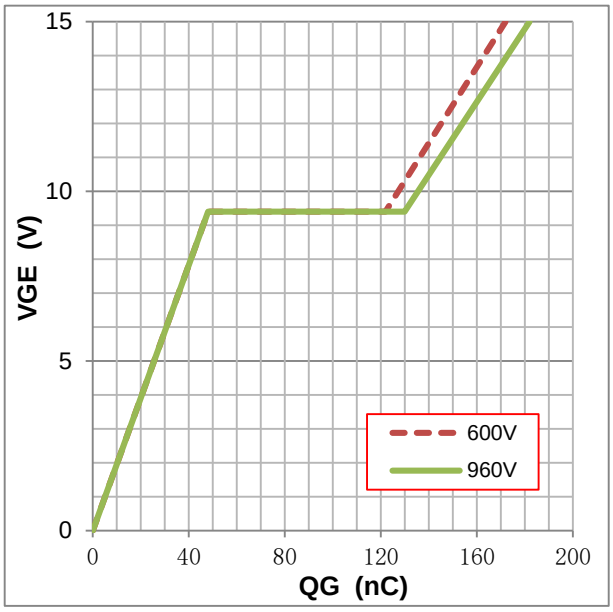


Figure18:typical gate charge VS. VGE,IC=50A

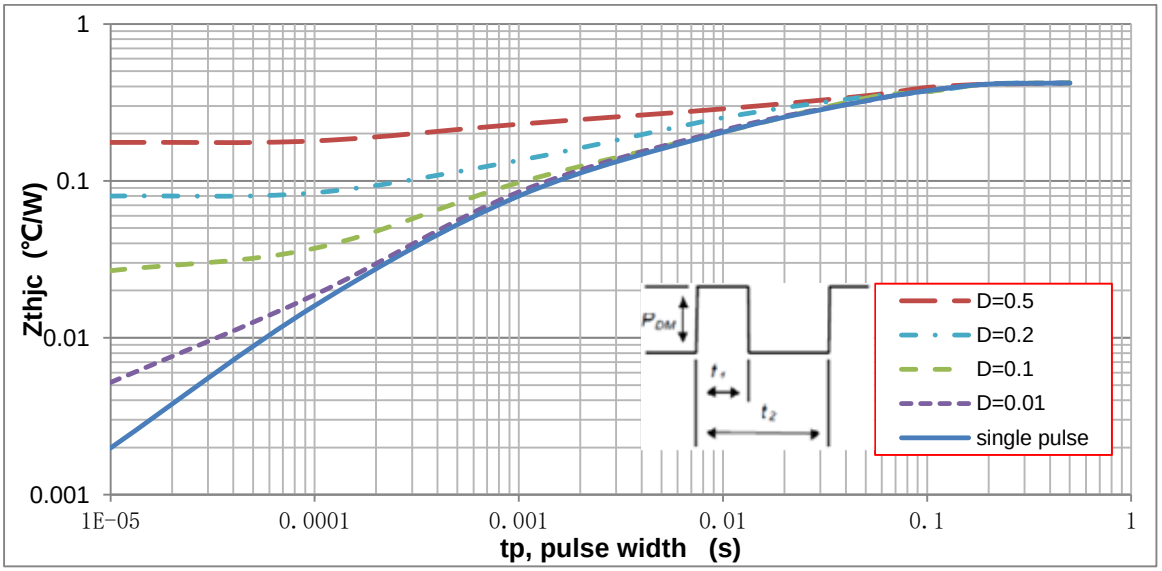
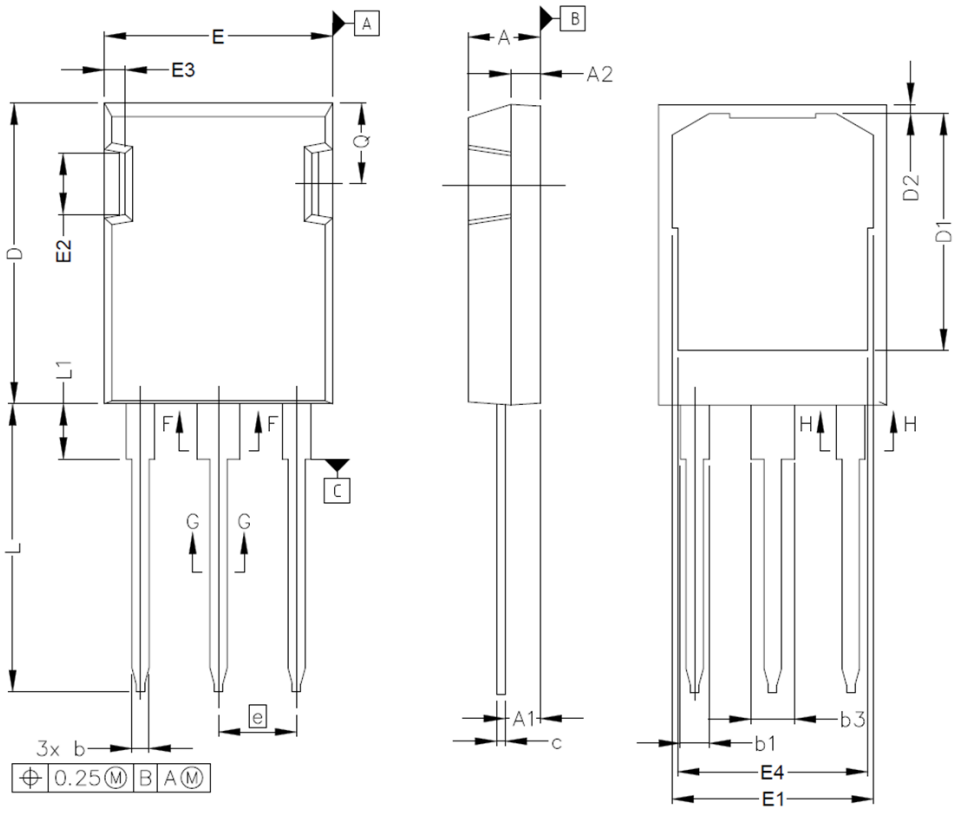


Figure19:normalized transient thermal impedance, junction-to-case

Note1.Duty factor D=t1/t2; Note2:peak TJ=PDM×Zthjc+TC

封装外形



SYMBOL	MIN	MAX
A	4.83	5.21
A1	2.29	2.54
A2	1.91	2.16
b'	1.07	1.28
b	1.07	1.33
b1	1.91	2.41
b2	1.91	2.16
b3	2.87	3.38
b4	2.87	3.13
c'	0.55	0.65
c	0.55	0.68
D	20.80	21.10
D1	16.25	17.65
D2	0.50	0.80

SYMBOL	MIN	MAX
E	15.75	16.13
E1	13.10	14.15
E2	3.68	5.10
E3	1.00	1.90
E4	12.38	13.43
e	5.44 BSC	
N	3	
L	19.81	20.32
L1	3.70	4.00
Q	5.49	6.00

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