

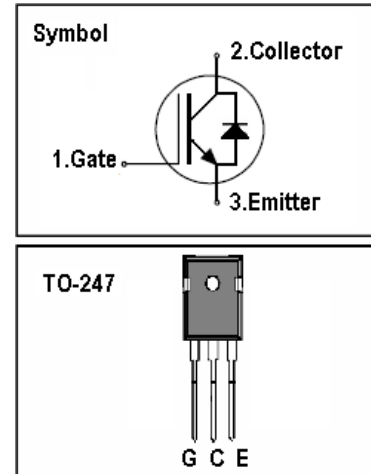
## IGBT

### Features

- 1200V,40A
- $V_{CE(sat)(typ.)} = 1.85V @ V_{GE} = 15V, I_C = 40A$
- High speed switching
- Higher system efficiency
- Soft current turn-off waveforms

### General Description

JIAEN Trench IGBTs offer lower losses and higher energy efficiency for application such as induction heating, UPS, AC & DC motor controls and general purpose inverter .



### Absolute Maximum Ratings ( $T_C = 25^\circ C$ unless otherwise noted)

Symbol	Parameter	Value	Units
$V_{CES}$	Collector-Emitter Voltage	1200	V
$V_{GES}$	Gate-Emitter Voltage	$\pm 30$	V
$I_C$	Continuous Collector Current ( $T_C=25^\circ C$ )	80	A
	Continuous Collector Current ( $T_C=100^\circ C$ )	40	A
$I_{CM}$	Pulsed Collector Current (Note 1)	160	A
$I_F$	Diode Continuous Forward Current ( $T_C=100^\circ C$ )	40	A
$I_{FM}$	Diode Maximum Forward Current (Note 1)	160	A
$t_{sc}$	Short Circuit Withstand Time	10	us
$P_D$	Maximum Power Dissipation ( $T_C=25^\circ C$ )	415	W
	Maximum Power Dissipation ( $T_C=100^\circ C$ )	165	W
$T_J$	Operating Junction Temperature Range	-50 to +150	$^\circ C$
$T_{STG}$	Storage Temperature Range	-50 to +150	$^\circ C$

### Thermal Characteristics

Symbol	Parameter	Max.	Units
$R_{th\ j-c}$	Thermal Resistance, Junction to case for IGBT	0.29	$^\circ C / W$
$R_{th\ j-c}$	Thermal Resistance, Junction to case for Diode	0.65	$^\circ C / W$
$R_{th\ j-a}$	Thermal Resistance, Junction to Ambient	40	$^\circ C / W$

**Electrical Characteristics** ( $T_C=25^\circ\text{C}$  unless otherwise noted)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
$BV_{CES}$	Collector-Emitter Breakdown Voltage	$V_{GE}=0V, I_C=250\mu A$	1200	-	-	V
$I_{CES}$	Collector-Emitter Leakage Current	$V_{CE}=1200V, V_{GE}=0V$	-	-	100	$\mu A$
$I_{GES}$	Gate Leakage Current, Forward	$V_{GE}=30V, V_{CE}=0V$	-	-	100	nA
	Gate Leakage Current, Reverse	$V_{GE}=-30V, V_{CE}=0V$	-	-	-100	nA
$V_{GE(th)}$	Gate Threshold Voltage	$V_{GE}=V_{CE}, I_C=250\mu A$	4.5	-	6.5	V
$V_{CE(sat)}$	Collector-Emitter Saturation Voltage	$V_{GE}=15V, I_C=40A$	-	1.85	2.5	V
		$V_{GE}=15V, I_C=40A$ $T_C=125^\circ\text{C}$		2.05		
$t_{d(on)}$	Turn-on Delay Time	$V_{CC}=600V$ $V_{GE}=15V$ $I_C=40A$ $R_G=15\Omega$ Inductive Load $T_C=25^\circ\text{C}$	-	85	-	ns
$t_r$	Turn-on Rise Time		-	50	-	ns
$t_{d(off)}$	Turn-off Delay Time		-	470	-	ns
$t_f$	Turn-off Fall Time		-	210	-	ns
$E_{on}$	Turn-on Switching Loss		-	2.8	-	mJ
$E_{off}$	Turn-off Switching Loss		-	3.6	-	mJ
$E_{ts}$	Total Switching Loss		-	6.4	-	mJ
$C_{ies}$	Input Capacitance		$V_{CE}=30V$	-	4500	-
$C_{oes}$	Output Capacitance	$V_{GE}=0V$	-	190	-	pF
$C_{res}$	Reverse Transfer Capacitance	$f=1\text{MHz}$	-	90	-	pF
$Q_g$	Total Gate Charge	$V_{CC}=600V$	-	380		nC
$Q_{ge}$	Gate-Emitter Charge	$V_{GE}=15V$	-	25		nC
$Q_{gc}$	Gate-Collector Charge	$I_C=40A$	-	215		nC

**Electrical Characteristics of Diode** ( $T_C=25^\circ\text{C}$  unless otherwise noted)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
$V_F$	Diode Forward Voltage	$I_F=40A$	-	2.0	2.8	V
$t_{rr}$	Diode Reverse Recovery Time	$V_{CE}=400V$	-	200		ns
$I_{rr}$	Diode peak Reverse Recovery Current	$I_F=40A$	-	16		A
$Q_{rr}$	Diode Reverse Recovery Charge	$dI_F/dt=200A/\mu s$	-	1800		nC

**Notes:**

1. Repetitive Rating: Pulse width limited by maximum junction temperature

## Typical Performance Characteristics

Figure 1. Typical Output Characteristics

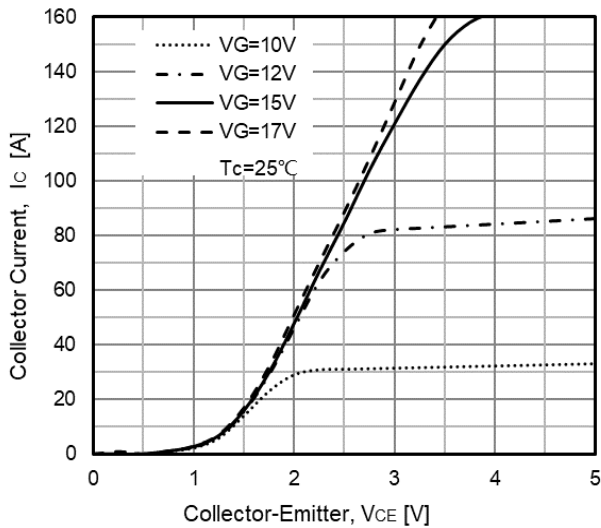


Figure 2. Typical Saturation Voltage Characteristics

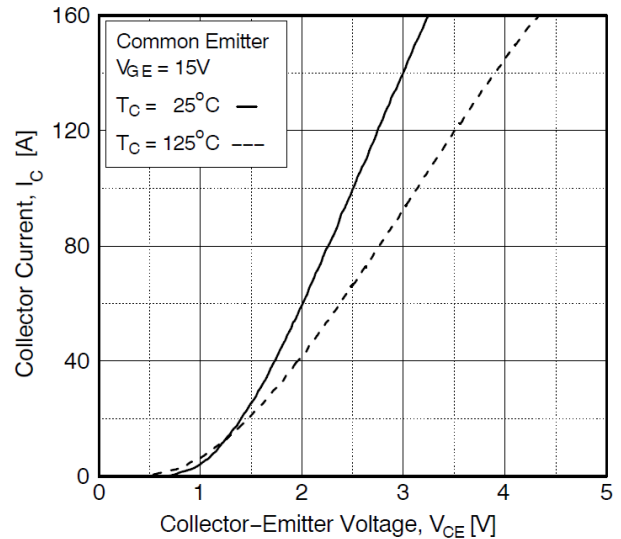


Figure 3. Saturation Voltage vs. Case Temperature at Variant Current Level

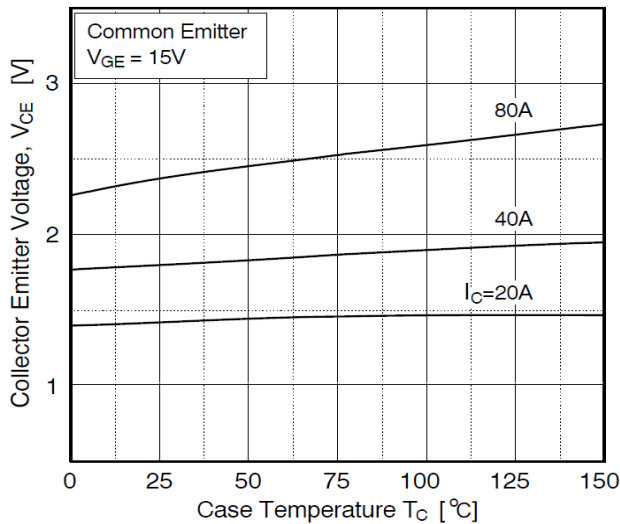
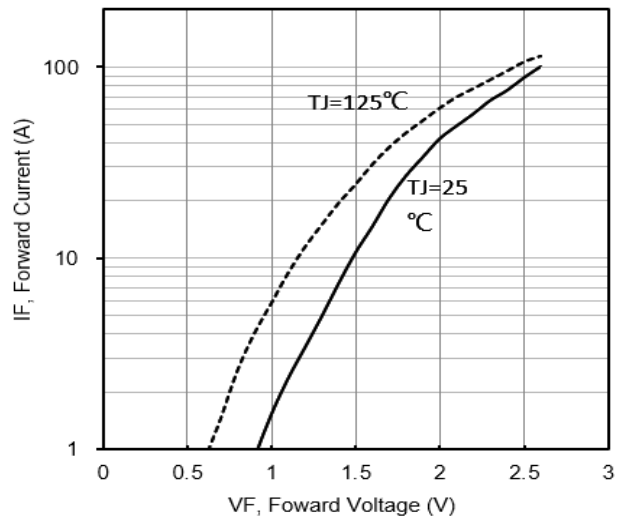


Figure 4. Forward Characteristics



## Typical Performance Characteristics

Figure 5. Saturation Voltage vs. V<sub>GE</sub>

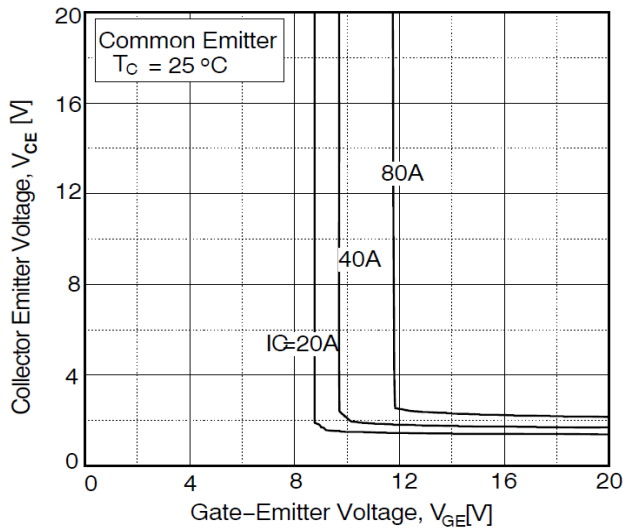


Figure 6. Saturation Voltage vs. V<sub>GE</sub>

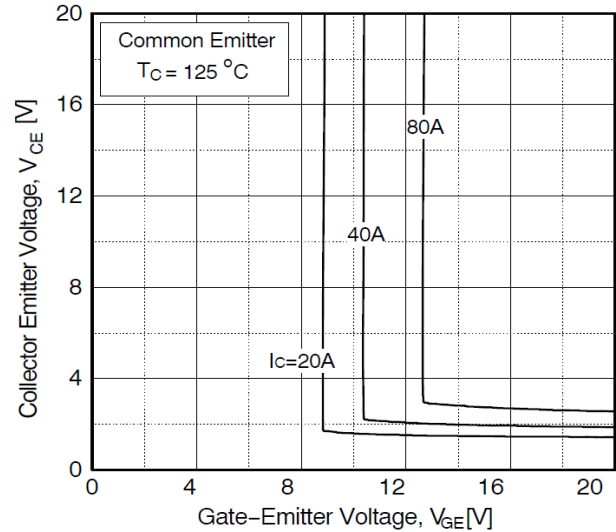


Figure 7. Switching Loss vs. Gate Resistance

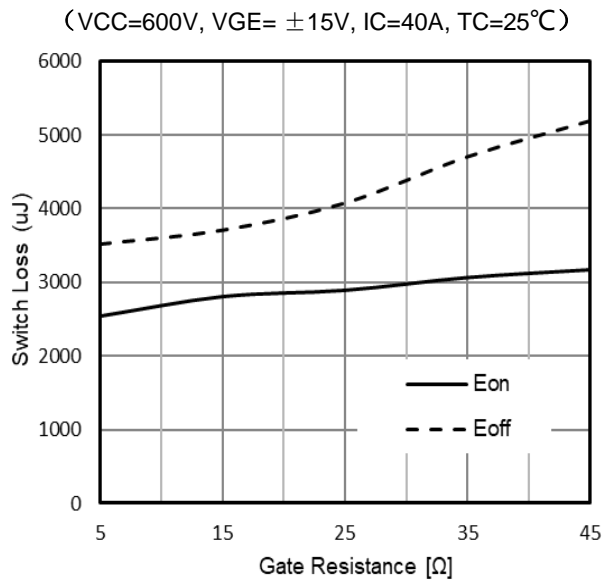
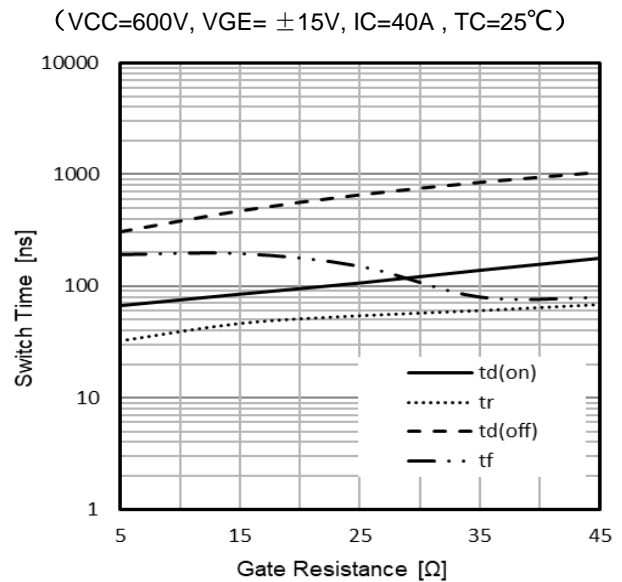


Figure 8. Switch time vs. Gate Resistance



## Typical Performance Characteristics

Figure 9. Switch Time vs. Collector Current

( $V_{GE} = \pm 15V$ ,  $R_G = 5 \text{ Ohm}$ ,  $V_{CC} = 600V$ ,  $T_C = 25^\circ C$ )

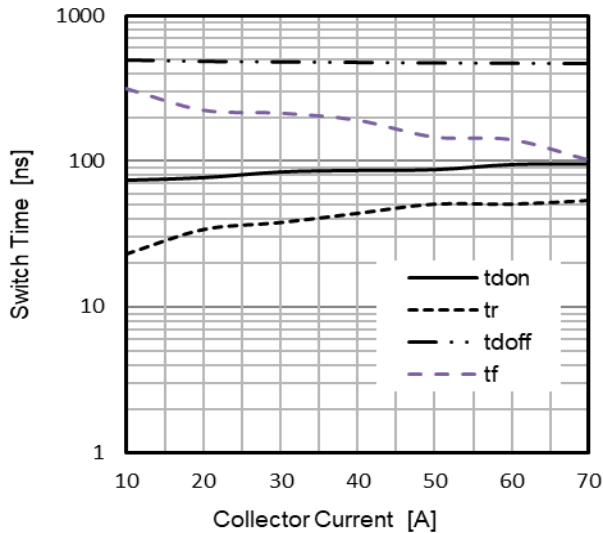


Figure 10. Capacitance Characteristics

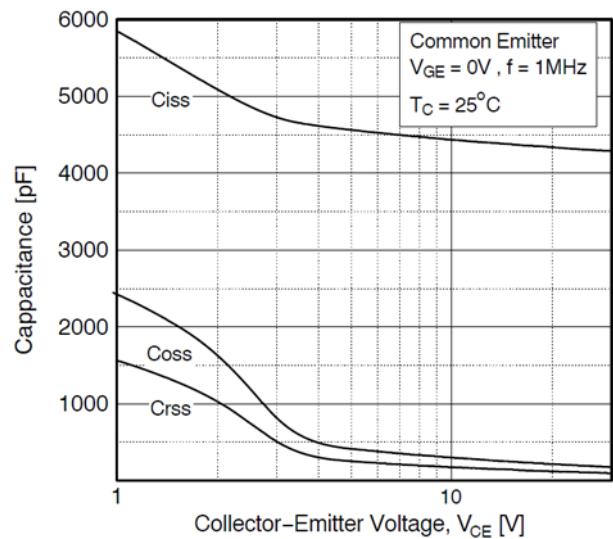


Figure 11. Gate Charge Characteristics

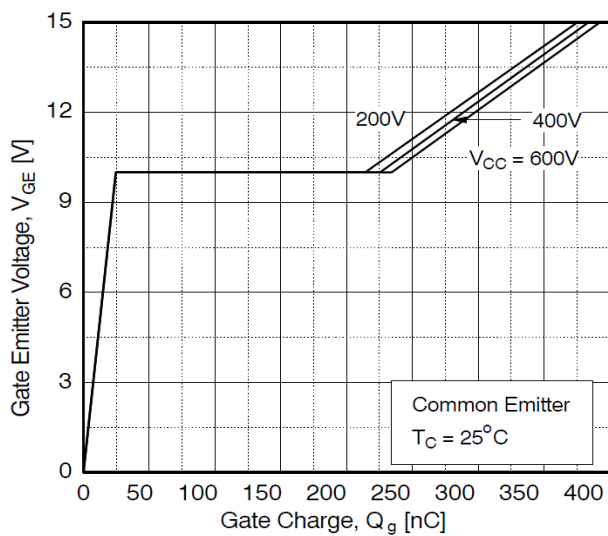
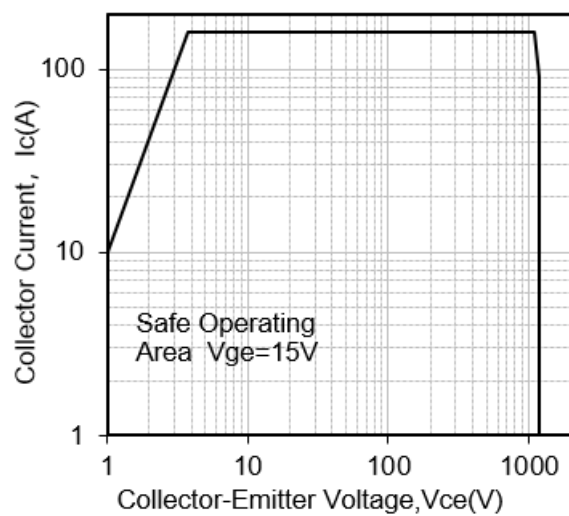


Figure 12. Turn Off SOA



## Typical Performance Characteristics

Figure 13. SOA Characteristics

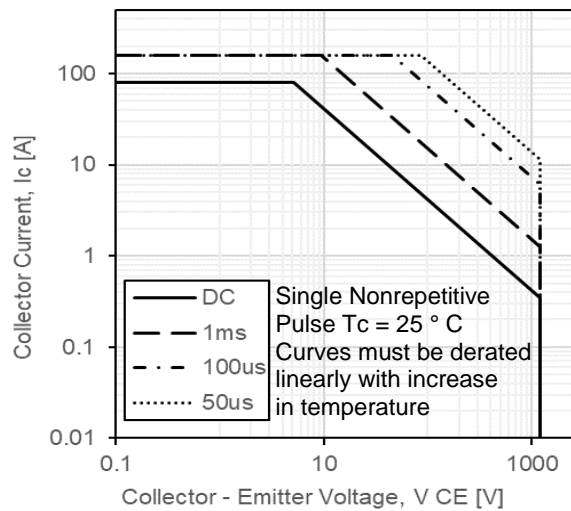
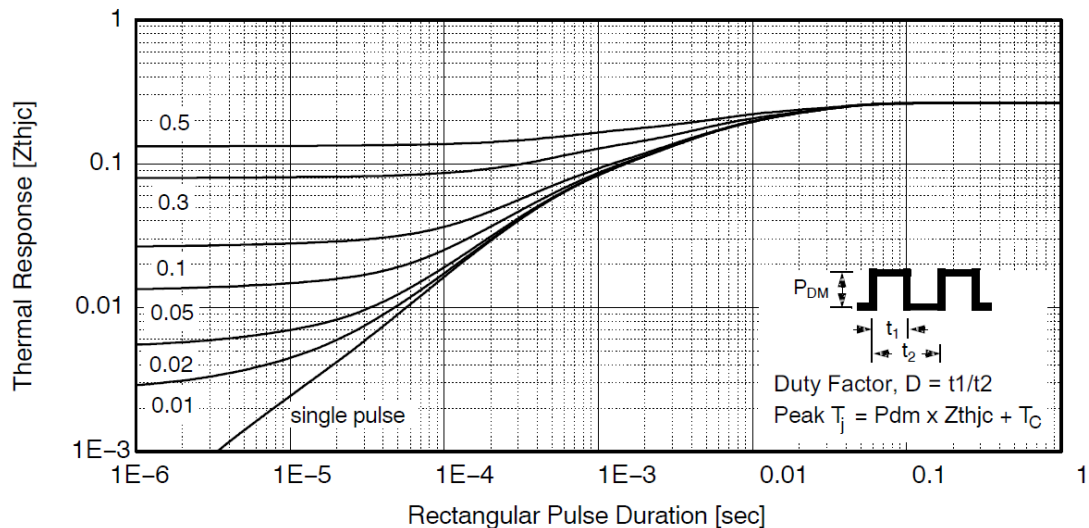
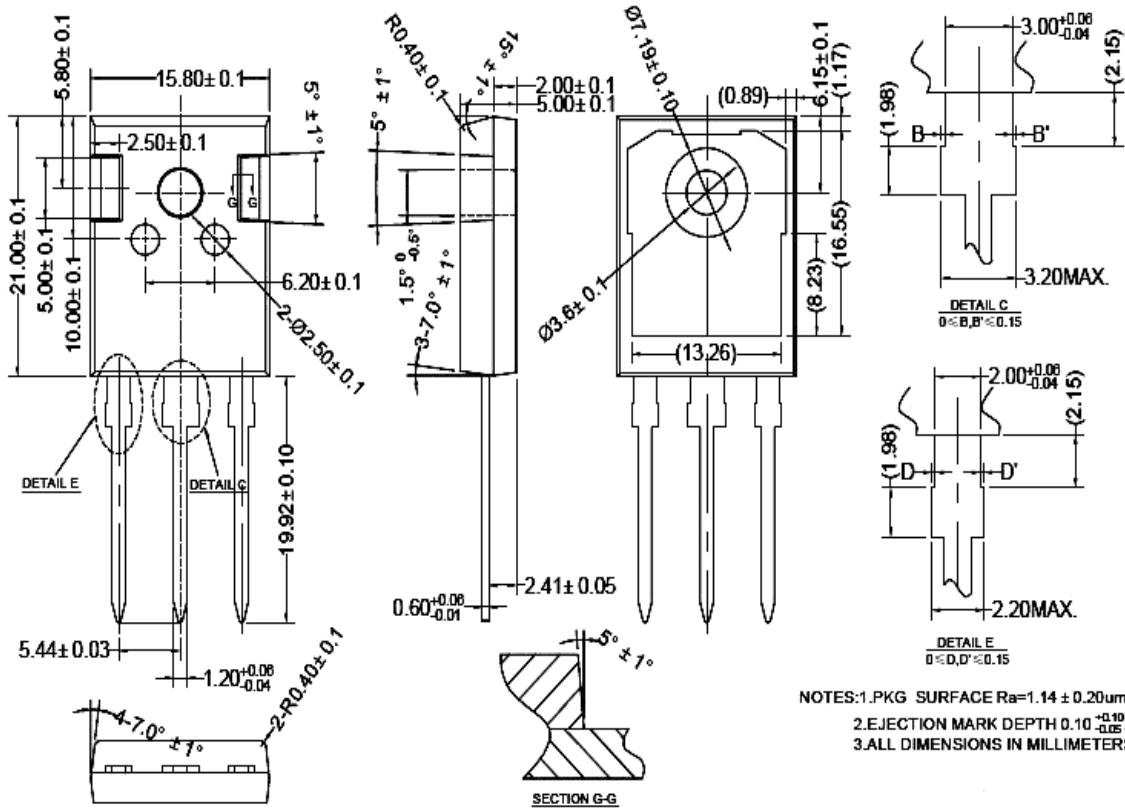


Figure 14. Transient Thermal Impedance of IGBT



TO247 PACKAGE OUTLINE



NOTES:1.PKG SURFACE Ra=1.14 ± 0.20um.  
2.EJECTION MARK DEPTH 0.10<sup>+0.10</sup>/<sub>-0.05</sub>.  
3.ALL DIMENSIONS IN MILLIMETERS.

REVISIONS

公差标注	公差值	表面粗糙度
0	±0.2	Ra3.2~6.3
0.0	±0.1	Ra1.6~3.2
0.00	±0.01	Ra0.8~1.6
0.000	±0.005	Ra0.4~0.8
0.0000	±0.002	Ra0.2~0.4

0≤D,D'≤0.15

NOTES:1.PKG SURFACE Ra=1.14 ± 0.20um.  
2.EJECTION MARK DEPTH 0.10<sup>+0.10</sup>/<sub>-0.05</sub>.  
3.ALL DIMENSIONS IN MILLIMETERS.

## **Disclaimers**

JIAEN Semiconductor Co., Ltd reserves the right to make changes without notice in order to improve reliability, function or design and to discontinue any product or service without notice. Customers should obtain the latest relevant information before orders and should verify that such information is current and complete. All products are sold subject to JIAEN's terms and conditions supplied at the time of order acknowledgement.

JIAEN Semiconductor Co., Ltd warrants performance of its hardware products to the specifications at the time of sale, Testing, reliability and quality control are used to the extent JIAEN deems necessary to support this warrantee. Except where agreed upon by contractual agreement, testing of all parameters of each product is not necessarily performed.

JIAEN Semiconductor Co., Ltd does not assume any liability arising from the use of any product or circuit designs described herein. Customers are responsible for their products and applications using JIAEN's components. To minimize risk, customers must provide adequate design and operating safeguards.

JIAEN Semiconductor Co., Ltd does not warrant or convey any license either expressed or implied under its parent rights, nor the rights of others. Reproduction of information in JIAEN's datasheets or data books is permissible only if reproduction is without modification or alteration. Reproduction of this information with any alteration is an unfair and deceptive business practice. JIAEN Semiconductor Co., Ltd is not responsible or liable for such altered documentation.

Resale of JIAEN's products with statements different from or beyond the parameters stated by JIAEN Semiconductor Co., Ltd for that product or service voids all express or implied warranties for the associated JIAEN's product or service and is unfair and deceptive business practice. JIAEN Semiconductor Co., Ltd is not responsible or liable for any such statements.